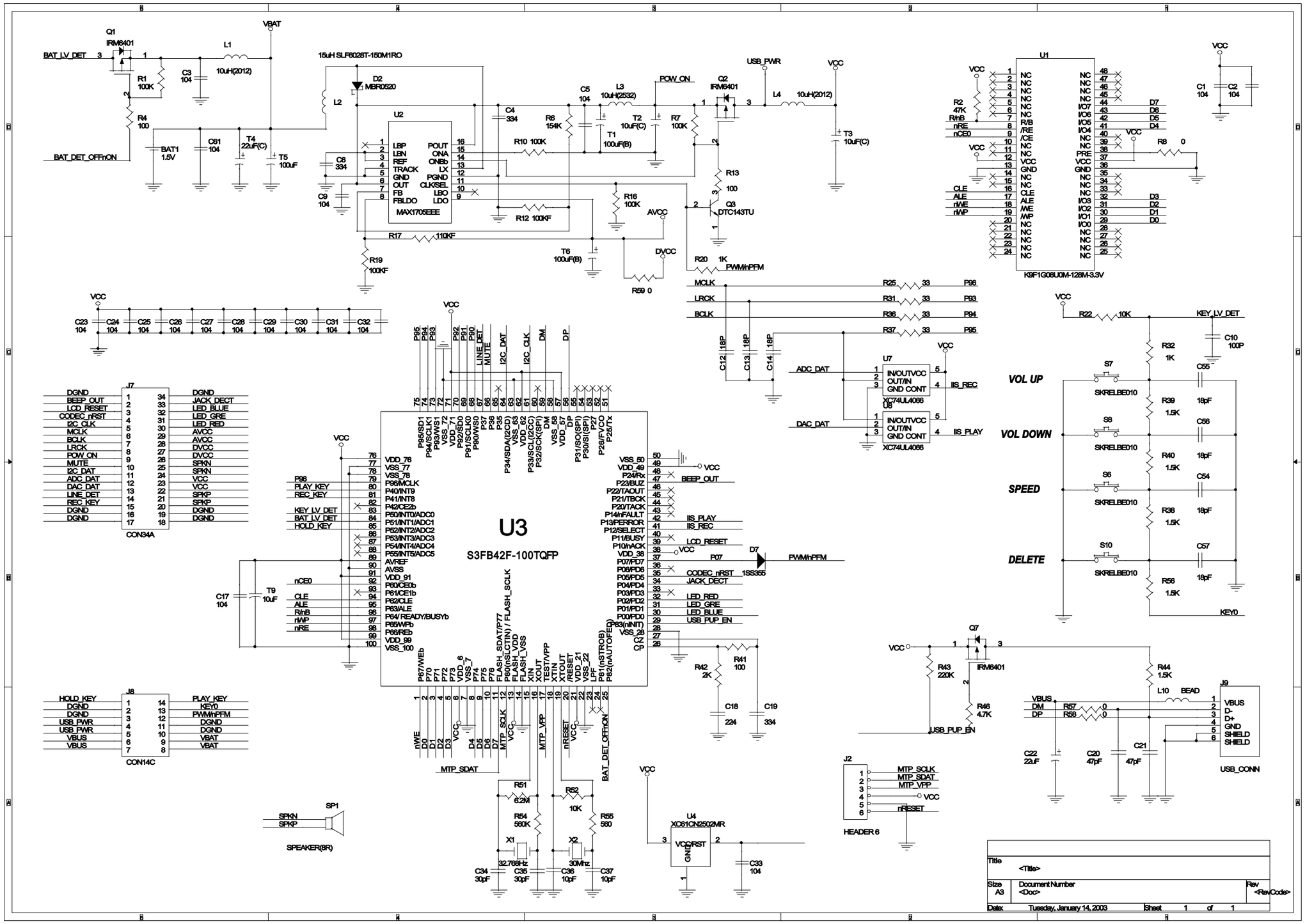
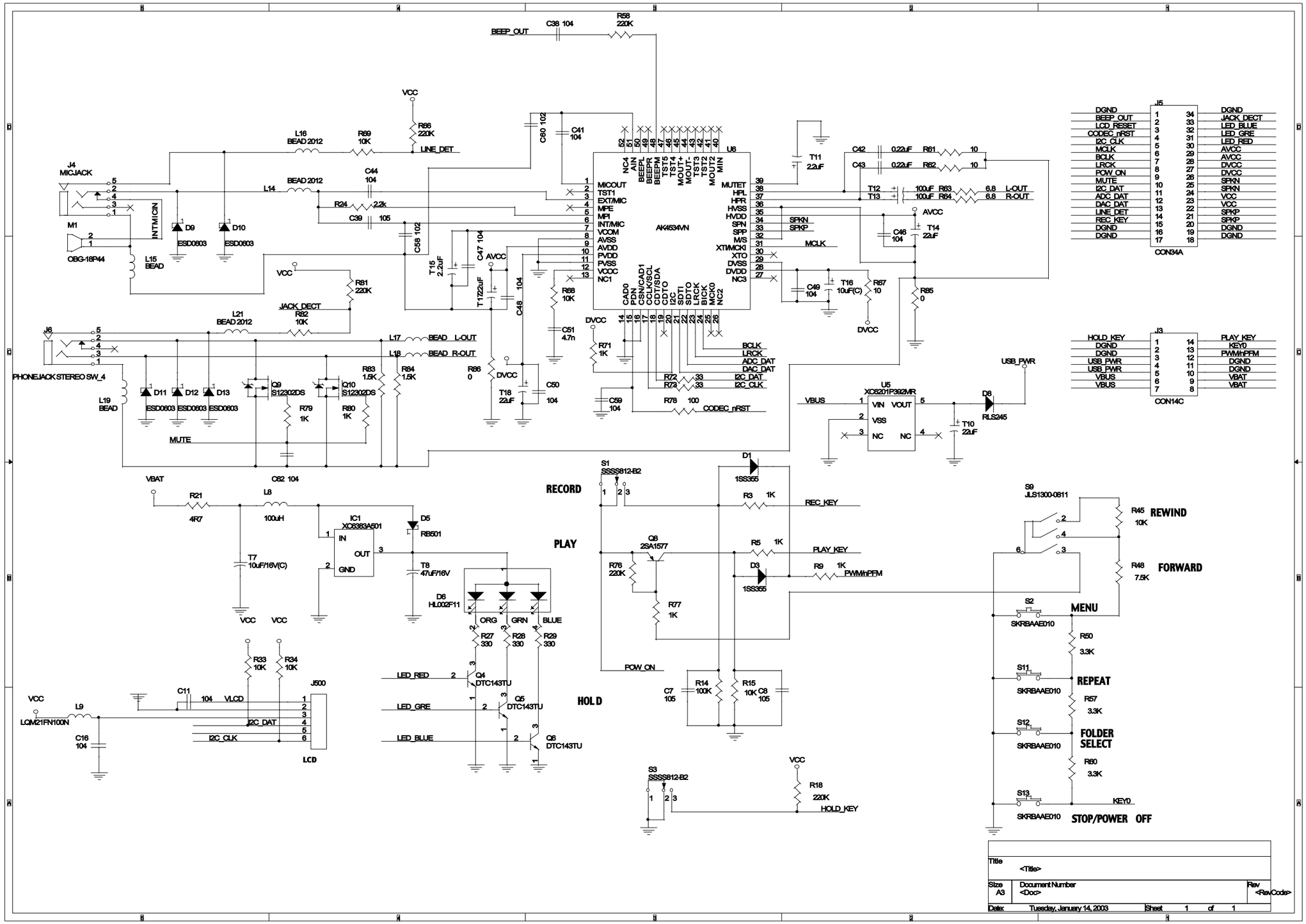
5. Schematic Diagram

5-1 MAIN




BEEP_OUT
C38 104
R58 220K
VCC
R66 220K
LINE_DET
L16 BEAD 2012
R89 10K
C60 102
C41 104
U6
AK4534VN
MICOUT
TST1
EXT/MIC
MPE
MPI
INT/MIC
VCOM
AVSS
AVDD
PVDD
PVSS
VCCC
NC1
MUTET
HPL
HPR
HVSS
HVDD
SPN
SPP
MS
XTI/MCKI
XTO
DVSS
DVDD
NC3
SPKN
SPKP
MCLK
T11 2.2uF
C42 0.22uF R61 10
C43 0.22uF R62 10
T12 100uF R63 6.8 L-OUT
T13 100uF R64 6.8 R-OUT
AVCC
C46 104
T14 22uF
J4
MICJACK
M1
OBG-18P44
INTMICIN
L15 BEAD
D9 ESD0603
D10 ESD0603
L14 BEAD 2012
C44 104
R24 2.2k
C39 105
C58 102
T15 2.2uF
C47 104
T17 22uF
C48 104
R68 10K
C51 4.7n
R81 220K
JACK_DECT
R82 10K
L21 BEAD 2012
J6
PHONEJACK STEREO SW_4
L19 BEAD
D11 ESD0603
D12 ESD0603
D13 ESD0603
Q9 S12302DS
Q10 S12302DS
R79 1K
R80 1K
R83 1.5K
R84 1.5K
R86 0
L17 BEAD L-OUT
L18 BEAD R-OUT
MUTE
DVCC
R71 1K
C50 104
T18 22uF
C59 104
R78 100
CODEC_nRST
BCLK
LRCK
ADC_DAT
DAC_DAT
I2C_DAT
I2C_CLK
R72 33
R73 33
C49 104
T16 10uF(C)
R67 10
R85 0
USB_PWR
U5
XC6201P382MR
VIN
VOUT
VSS
NC
D8 RLS245
T10 22uF
VBAT
C62 104
R21 4R7
L8 100uH
T7 10uF/16V(C)
IC1 XC6383A501
IN
OUT
GND
D5 RB501
T8 47uF/16V
D6 HL002F11
ORG
GRN
BLUE
R27 330
R28 330
R29 330
LED_RED
LED_GRE
LED_BLUE
Q4 DTC143TU
Q5 DTC143TU
Q6 DTC143TU
R33 10K
R34 10K
C11 104 VLCD
I2C_DAT
I2C_CLK
J500
LCD
L9 LQM21FN100N
C16 104
RECORD
PLAY
HOLD
S1 SSSS812-B2
D1 1SS355
R3 1K
REC_KEY
Q8 2SA1577
R5 1K
PLAY_KEY
R76 220K
R77 1K
D3 1SS355
R9 1K
PWM/nPFM
POW_ON
C7 105
R14 100K
R15 10K
C8 105
S3 SSSS812-B2
R18 220K
HOLD_KEY
S9 JLS1300-0811
R45 10K
REWIND
R48 7.5K
FORWARD
S2 SKRBAAE010
MENU
R50 3.3K
S11 SKRBAAE010
REPEAT
R57 3.3K
S12 SKRBAAE010
FOLDER SELECT
R60 3.3K
S13 SKRBAAE010
KEY0
STOP/POWER OFF
J5
CON34A
DGND
BEEP_OUT
LCD_RESET
CODEC_nRST
I2C_CLK
MCLK
BCLK
LRCK
POW_ON
MUTE
I2C_DAT
ADC_DAT
DAC_DAT
LINE_DET
REC_KEY
JACK_DECT
LED_BLUE
LED_GRE
LED_RED
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DVCC
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Date: Tuesday, January 14, 2003
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